

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3203155	memory or storage	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/21 10:14
L2	2823	1 and split adj gate	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/21 10:15
L3	3	2 and split adj source	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/21 10:16
L4	7	2 and different adj source adj line	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/21 10:20
L7	6	2 and separate adj source adj line	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/21 10:36
L8	44	2 and segment with (word or wordline)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/21 10:40
L9	0	2 and segment with (word or wordline) with driver	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/21 10:37
L10	8	2 and (segment or part or portion) with (word or wordline) with driv\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/21 10:48
L11	3522	1 and (segment or part or portion) with (word or wordline) with driv\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/21 10:49
L12	852	11 and (nonvolatile or non-volatile or floatng adj gate or flash)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/21 10:51
L13	28	12 and split near3 gate	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/21 11:44
L14	103	2 and ("3" or three) adj3 transistor with cell	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/21 11:55

L17	48	14 and program\$6 adj3 (bit or bitline)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/21 11:56
L19	25	17 and read\$5 adj3 (bit or bitline)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/21 11:48
L20	333	2 and ("2" or two) adj3 transistor with cell	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/21 11:55
L21	21	20 and program\$6 adj3 (bit or bitline) with read\$5 adj3 (bit or bitline)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/21 11:56